

WHAT IS CLAIMED IS:

1. A display apparatus, including:

 a pixel circuit;

 a display control circuit which controls said pixel circuit; and

 a supply region for supplying a predetermined voltage used to drive said pixel circuit,

 wherein said pixel circuit, said display control circuit and said supply region are formed by a stacking process in a manner such that at least part of said display control circuit overlaps with said supply region in a stacking direction thereof.

2. A display apparatus according to Claim 1, wherein said

pixel circuit is arranged in a matrix so as to form a display region, and said display control circuit outputs a data signal in a column direction thereof.

3. A display apparatus according to Claim 1, wherein said

pixel circuit is arranged in a matrix so as to form a display region, and said display control circuit outputs a selection signal in a row direction thereof.

4. A display apparatus, including:

 a display region in which a plurality of pixel

circuits are arranged in a matrix;

a display control circuit, disposed outside said display region, which controls the plurality of pixel circuits in a row or column direction;

a supply region for supplying a predetermined voltage used to drive the plurality of pixel circuits; and

a connecting member which electrically connects the display apparatus to an external unit, so as to introduce a signal to be referred to by said display control circuit and the predetermined voltage,

wherein said pixel circuit, said display control circuit and said supply region are formed by a stacking process in a manner such that at least part of said display control circuit overlaps with said supply region in layers from said connecting member up to said supply region in a stacking direction thereof.

5. A display apparatus, including:

a pixel circuit;

a display control circuit which controls said pixel circuit; and

a supply region for supplying a predetermined voltage used to drive said pixel circuit,

wherein said pixel circuit, said display control circuit and said supply region are formed in a plurality of stacking layers and formed in a manner such that at least

part of said display control circuit overlaps with said supply region in a stacking direction thereof.

6. A display apparatus according to Claim 1, wherein said display control circuit includes at least one of a shift register, a buffer and a switching circuit and wherein the at least one element is formed in such a manner as to overlap with said supply region supplying the predetermined voltage.

7. A display apparatus according to Claim 4, wherein said display control circuit includes at least one of a shift register, a buffer and a switching circuit and wherein the at least one element is formed in such a manner as to overlap with said supply region supplying the predetermined voltage.

8. A display apparatus according to Claim 5, wherein said display control circuit includes at least one of a shift register, a buffer and a switching circuit and wherein the at least one element is formed in such a manner as to overlap with said supply region supplying the predetermined voltage.

9. A display apparatus according to Claim 4, wherein the signal to be referred to is at least one of luminance data,

a clock signal and a start signal.

10. A display apparatus according to Claim 4, wherein said electrically connecting member is an external input terminal and wherein said supply region supplying the predetermined voltage is formed in such a manner as to overlap with said display control circuit or a wiring region for the signal to be referred to in a layer from the external input terminal up to said display region.

11. A display apparatus according to Claim 4, wherein at least part of a region, in which the signal to be referred to is interconnected, is formed in such a manner as to overlap with said supply region supplying the predetermined voltage.

12. A display apparatus according to Claim 4, wherein a plurality of said connecting members are provided in the periphery of the display apparatus, a plurality of said display control circuits are provided according to the number of said connecting members, the number of the signals to be referred to and a region thereof to be interconnected are determined according to the number of said connecting members and display control circuits, and wherein said display control circuits, the wiring region and said supply region are formed in a manner such that at least part of the

plurality of said display control circuits and the wiring region for the plurality of signals to be referred to is overlapped with said supply region in layers from the plurality of connecting members up to said display region.

13. A display apparatus according to Claim 4, wherein said connecting member is provided on either above or below the display apparatus, or is provided on both above and below the display apparatus, for one each or two each thereof.

14. A display according to Claim 4, wherein said connecting member is provided on both left and right sides of the display apparatus, and each of the thus provided connecting members includes a power supply terminal of anode and cathode.

15. A display apparatus according to Claim 12, wherein positions and layout of the plurality of connecting members, the plurality of interconnection for the signals to be referred to and the plurality of display control circuits are arranged in a manner such that vertical or horizontal symmetry is maintained.

16. A display apparatus according to Claim 12, wherein said supply region is formed in a manner such that at least one of the plurality of connecting members, the plurality of

interconnection for the signals to be referred to and the plurality of display control circuits is substantially uniformly overlapped with said supply region in a vertical or horizontal direction.

17. A display apparatus according to Claim 12, wherein said display region constituted by a plurality of pixel circuits are divided into sub-regions, and the plurality of display control circuits control the plurality of pixel circuits at separate timing for each of the sub-regions.

18. A display apparatus according to Claim 1, wherein said pixel circuit includes an organic light emitting diode as an optical element.

19. A display apparatus according to Claim 4, wherein said pixel circuit includes an organic light emitting diode as an optical element.

20. A display apparatus according to Claim 5, wherein said pixel circuit includes an organic light emitting diode as an optical element.